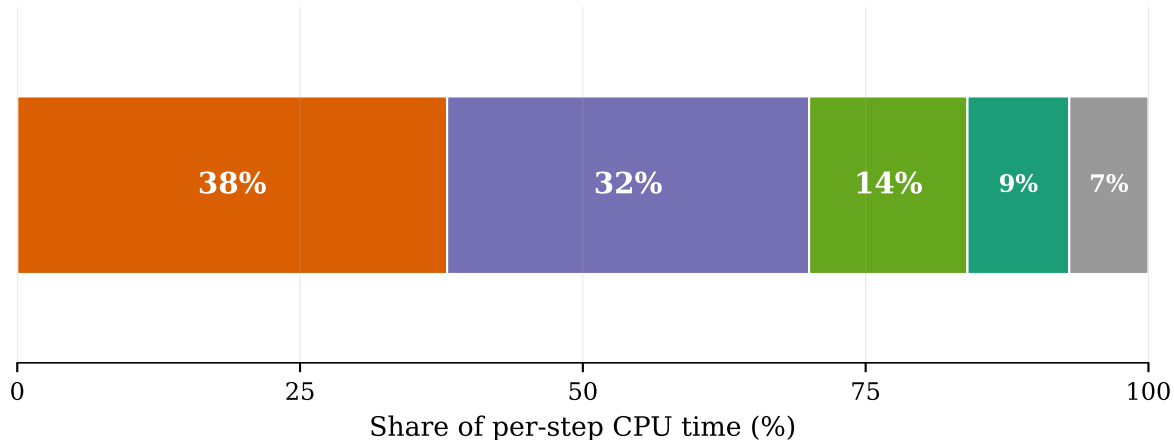


SPICE-style simulator: per-step CPU cost share  
(buck @ 100 kHz, 1000 cycles, dt=10 ns, profiled on ngspice 41)



- |                                     |  |
|-------------------------------------|--|
| Jacobian assembly (stamp_*)         | Triangular solve (fwd/back substitution) |
| Sparse LU factorisation (lu_decomp) | Misc. (history update, output)           |
| Newton iteration management         |  |