

PORT 1



LF

RF



LF



LF

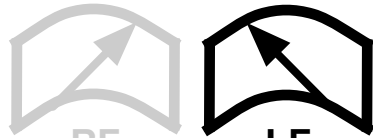


RF



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[DUT]



RF

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PORT 2



LF



LF



RF



RF