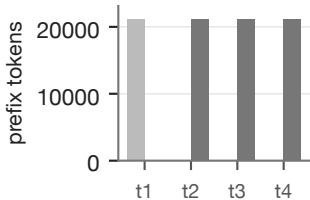
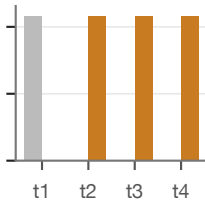


cache write      cache read

Direct



MLPal Gateway



LiteLLM proxy

