Authored for Fenolite (CC0-1.0). Fictional example; no real product.

EXB6202: pins 1 through 8 named P1 through P8; all passive.

SOIC variant: body 5 x 3 x 1 mm; span 6 mm; pitch 1.27 mm.

Leads 0.6 x 0.45 mm; land 1.1 x 0.7 mm on 5.5 mm rows.

Grid variant EXGRID8: 3 by 3 at 1 mm pitch, rows A B C; B2 absent.

Grid body 4 x 4 x 1 mm; balls and round lands 0.5 mm diameter.

Both variants: maximum input voltage 6 V; ordering EXB6202DR, reel.