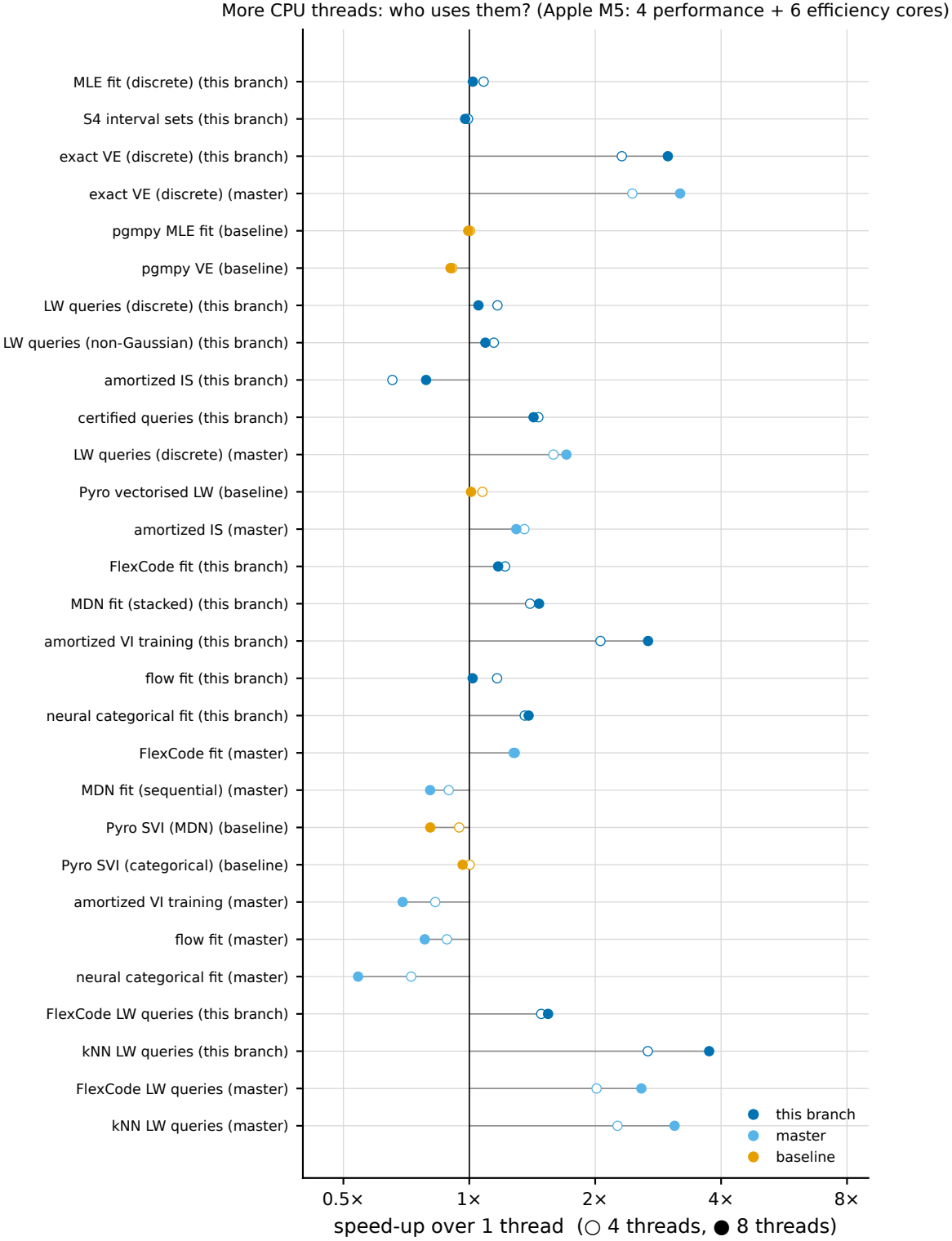

More CPU threads: who uses them? (Apple M5: 4 performance + 6 efficiency cores)
MLE fit (discrete) (this branch)
S4 interval sets (this branch)
exact VE (discrete) (this branch)
exact VE (discrete) (master)
pgmpy MLE fit (baseline)
pgmpy VE (baseline)
LW queries (discrete) (this branch)
LW queries (non-Gaussian) (this branch)
amortized IS (this branch)
certified queries (this branch)
LW queries (discrete) (master)
Pyro vectorised LW (baseline)
amortized IS (master)
FlexCode fit (this branch)
MDN fit (stacked) (this branch)
amortized VI training (this branch)
flow fit (this branch)
neural categorical fit (this branch)
FlexCode fit (master)
MDN fit (sequential) (master)
Pyro SVI (MDN) (baseline)
Pyro SVI (categorical) (baseline)
amortized VI training (master)
flow fit (master)
neural categorical fit (master)
FlexCode LW queries (this branch)
kNN LW queries (this branch)
FlexCode LW queries (master)
kNN LW queries (master)
this branch
master
baseline
0.5×
1×
2×
4×
8×
speed-up over 1 thread (○ 4 threads, ● 8 threads)